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FEB 21 2008

U.S. Serial No. 10/022,049

Response to the Office Action of December 12, 2007

This listing of claims will replace all prior versions, and listings, of claims in the application:

The Status of the Claims

1. (Currently amended) A system for organizing data for use by an application, comprising:
- a set top box, including,
 - a physical memory,
 - a mass storage device, and
 - a processor operatively connected to said physical memory and said mass storage device for implementing said application,
- wherein said physical memory and said mass storage device are configured to store said data,
- wherein said processor controls storage and manipulation of said data between said physical memory and said mass storage device,
- wherein the processor directs that the data be temporally sorted and stored by comparison of a current time to a time associated with the data, into data that is most likely to be immediately accessed for an application, and data that is most likely to be accessed in the more distant future, said data that is most likely to be immediately accessed being stored in said physical memory, said data that is most likely to be accessed in the more distant future being stored in said mass storage device; and
- wherein the processor controls storage and manipulation of said data so that necessary or relevant parts of said data may be accessed as the processor detects ~~usage scenarios~~ a user event likely to result in a cache miss, or when cache miss is not predicted.

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2. (Original) The system of claim 1, wherein said processor directs at least one process to add data from the mass storage device to said physical memory so as to maintain a sufficient amount of data to be accessed for the application.
3. (Original) The system of claim 2, wherein said processor directs another process to remove data from said mass storage device and said physical memory so as to ensure that the data used by said application can be accessed from said physical memory without delay.
4. (Previously presented) The system of claim 3, wherein said processes are run at a low priority so as not to interfere with the implementation with said application.
5. (Canceled)
6. (Previously presented) The system of claim 1, wherein said physical memory is subject to accesses by the processor to the data contained therein to run said application without having to access the mass storage device.
7. (Original) The system of claim 1, wherein the processor controls storage and manipulation of said data so that the size of the physical memory is configured based on a temporal window in said physical memory that encompasses most common usage scenarios employing the data.
8. (Previously presented) The system of claim 1, wherein the processor further detects that a user is scrolling in a program guide and is likely to need the data that is not currently stored in physical memory.

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9. (Original) The system of claim 1, wherein said data comprises program guide data for said application comprising a program guide for a plurality of program sources.

10. (Original) The system of claim 1, wherein said physical memory comprises a random access memory (RAM).

11. (Original) The system of claim 1, wherein said mass storage device comprises a hard disk.

12. (Original) The system of claim 9, further comprising a communications channel configured to transmit said program guide data to said set top box.

13. (Original) The system of claim 12, wherein said communications channel is configured as one of a satellite communications channel, a cable communications channel, a digital video broadcasting (DVB) communications channel and a terrestrial broadcast communications channel.

14. (Original) The system of claim 9, wherein said program guide is configured to display said program guide data on a display device coupled to said set top box in a tabular form including program times, program channels and program identifications.

15. (Original) The system of claim 14, wherein said program identifications include information regarding at least one of actors, ratings, description of programs, cost for pay per view, a frequency of said communications channel, a video channel within said frequency, and an audio channel within said frequency.

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16. (Currently amended) A method for organizing data for use by an application, comprising:

providing a set top box, including a physical memory and a mass storage device which are operatively coupled to a processor therein for implementing said application;

configuring said physical memory and said mass storage device to store said data;

controlling storage and manipulation of said data between said physical memory and said mass storage device so that the data to be used by said application is available for immediate access from said physical memory;

controlling storage and manipulation of said data between said physical memory and said mass storage device so that the data is temporally sorted and stored by comparison of a current time to a time associated with the data, into data that is most likely to be immediately accessed for an application, and data that is most likely to be accessed in the more distant future, said data that is most likely to be immediately accessed being stored in said physical memory, said data that is most likely to be accessed in the more distant future being stored in said mass storage device; and

controlling storage and manipulation of said data so that necessary or relevant portions of said data may be accessed as the processor detects ~~usage scenarios~~ a user event likely to result in a cache miss, or when cache miss is not predicted.

17. (Original) The method of claim 16, wherein said processor directs at least one low-priority background process to add data from the mass storage device to said physical memory so as to maintain a sufficient amount of data to be accessed for the application.

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18. (Original) The system of claim 17, wherein said processor directs another low-priority background process to remove data from said mass storage device and said physical memory so as to ensure that the data used by said application can be accessed from said physical memory without delay.

19. (Original) The method of claim 18, wherein said low-priority background processes are run so as not to interfere with the implementation with said application.

20. (Canceled)

21. (Previously presented) The method of claim 16, wherein said physical memory is subject to accesses by the processor to the data contained therein to run said application without having to access the mass storage device.

22. (Original) The method of claim 16, wherein the processor controls storage and manipulation of said data so that the size of the physical memory is configured based on a temporal window in said physical memory that encompasses most common usage scenarios employing the data.

23. (Currently amended) The method of claim 16, wherein ~~controlling the processor controls storage and manipulation of said data so that necessary or relevant portions of said data may be accessed as the processor detects usage scenarios likely to result in a cache miss, or when cache miss is not predicted, includes detecting~~ that a user is scrolling a program guide and likely needs data that is not currently in said physical memory.

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24. (Original) The method of claim 16, wherein said data comprises program guide data for said application comprising a program guide for a plurality of program sources.

25. (Original) The method of claim 16, wherein said physical memory comprises a random access memory (RAM).

26. (Previously presented) The method of claim 16, wherein said mass storage device comprises a hard disk.

27. (Original) The method of claim 24, further comprising a communications channel configured to transmit said program guide data to said set top box.

28. (Original) The method of claim 27, wherein said communications channel is configured as one of a satellite communications channel, a cable communications channel, a digital video broadcasting (DVB) communications channel and a terrestrial broadcast communications channel.

29. (Original) The method of claim 24, wherein said program guide is configured to display said program guide data on a device coupled to said set top box in a tabular form including program times, program channels and program identifications.

30. (Original) The method of claim 29, wherein said program identifications include information regarding at least one of actors, ratings, description of programs, cost for pay per view, a frequency of said communications channel, a video channel within said frequency, and an audio channel within said frequency.

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31. (Previously presented) A computer-readable medium carrying one or more sequences of one or more instructions for organizing data for use by an application, the one or more sequences of one or more instructions including instructions which, when executed by one or more processors, cause the one or more processors to perform the steps recited in any one of claims 16-19 or 21-30.

32. (Currently amended) A system for efficient storage of data for use by an application, comprising:

a set top box, including,

a physical memory,

a mass storage device, and

a processor directing at least two low-priority background process, one to add data from the mass storage device to said physical memory, the other to remove data from said mass storage device and said physical memory, so as to ensure that the data used by said application can be accessed from said physical memory without delay wherein the processor directs that the data be temporally sorted and stored by comparison of a current time to a time associated with the data, into data that is most likely to be immediately accessed for an application, and data that is most likely to be accessed in the more distant future, said data that is most likely to be immediately accessed being stored in said physical memory, said data that is most likely to be accessed in the more distant future being stored in said mass storage device; and

wherein the processor controls storage and manipulation of said data so that necessary or relevant parts of said data may be accessed as the processor detects ~~usage scenarios~~ a user event likely to result in a cache miss, or when cache miss is not predicted.